

REF ID: **UNDER 37 C.F.R § 1.116**
EXPEDITED PROCEDURE
EXAMINING GROUP 1753

PATENT
(5298-03500/PM99021)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Lau

Serial No. 09/476,669

Filed: December 30, 1999

For: **METHOD FOR FORMING A
METALLIZATION STRUCTURE IN
AN INTEGRATED CIRCUIT**

S Group Art Unit: 1753
S Examiner: Cantelmo, G.

S Atty. Dkt. No. 5298-03500

S I hereby certify that this correspondence is being deposited
S with the U.S. Postal Service with sufficient postage as First Class
S Mail in an envelope addressed to: Box AF, Commissioner for
S Patents, Washington, D.C. 20231, on the date indicated below:
S
S November 26, 2001
S Date
S Kevin L. Daffer
S
S

12/3
(4/03)
W.M.
1/9/03

AMENDMENT; RESPONSE AFTER FINAL REJECTION
PURSUANT TO 37 CFR § 1.116

*Entered
for RCT
Request
21/10/01
W.M.*
Box AF
Commissioner for Patents
Washington, D.C. 20231

Dear Sir/Madam:

This paper is submitted in response to the Office Action mailed September 26, 2001 to further highlight reasons why the application is in condition for allowance.

Please amend the case as follows.